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U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE  
STATEMENT

Docket Number  
10191/4186

Application Number  
To Be Assigned

Filing Date  
Herewith

Examiner  
To Be Assigned

Art Unit  
To Be Assigned

Invention Title  
ELECTRONIC CONTROL UNIT AND METHOD  
FOR SPECIFYING A SOFTWARE ARCHITECTURE  
FOR AN ELECTRONIC CONTROL UNIT

Inventor(s)  
ZURAWKA et al.

Commissioner for Patents  
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1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following references to the attention of the Examiner. The references are listed on the attached modified PTO form 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated on the modified PTO form 1449.

Dated: 12/20/05

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO FORM 1449</b>	Atty. Docket No. 10191/4186	Serial No. <b>10/561737</b> To Be Assigned
	Applicant(s) ZURAWKA et al.	
	Filing Date Herewith	Group To Be Assigned

**U. S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE

**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
						YES	NO
	197 50 662*	May 27, 1999	Germany				
	100 12 272*	Sep. 27, 2001	Germany				
	199 21 065*	Nov. 9, 2000	Germany				
	100 00 997*	Jan. 4, 2001	Germany				

\* Copy of reference is not enclosed because reference is cited in Search Report (copy of reference provided by International Searching Authority).

**OTHER DOCUMENTS**

EXAMINER'S INITIALS		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
		Sungjoo Yoo et al., <i>Building Fast and Accurate SW Simulation Models Based on Hardware Abstraction Layer and Simulation Environment Abstraction Layer</i> , Design, Automation and Test In Europe Conference and Exhibition, 2003 Munich, Germany, Mar. 3-7, 2003.; IEEE Comput. Soc. Los Alamitos, CA, mar. 3,2003, pgs. 550-5.*

\* Copy of reference is not enclosed because reference is cited in Search Report (copy of reference provided by International Searching Authority).

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	